



1 / 10

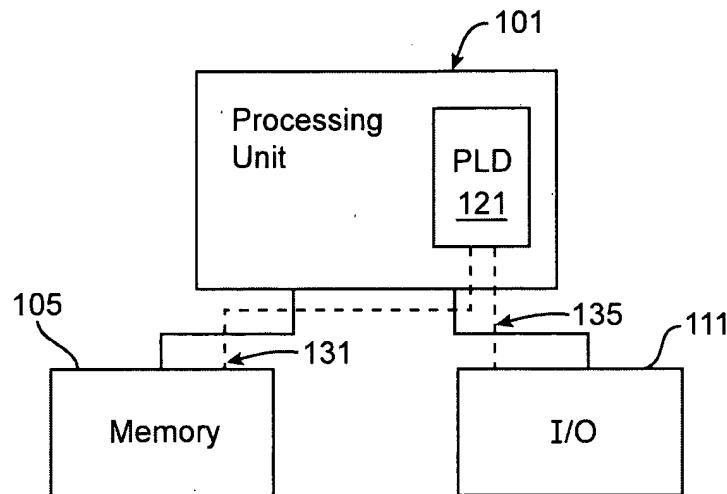


FIG. 1

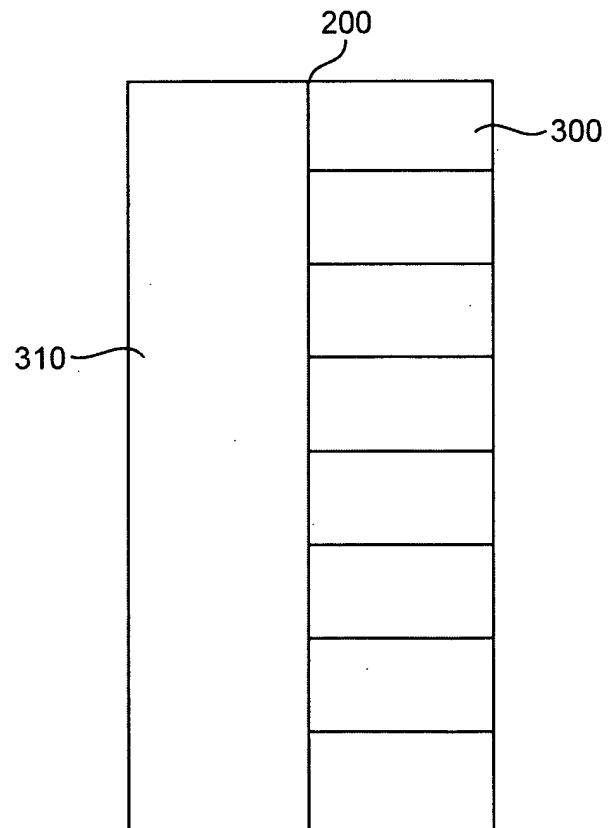


FIG. 3

2 / 10

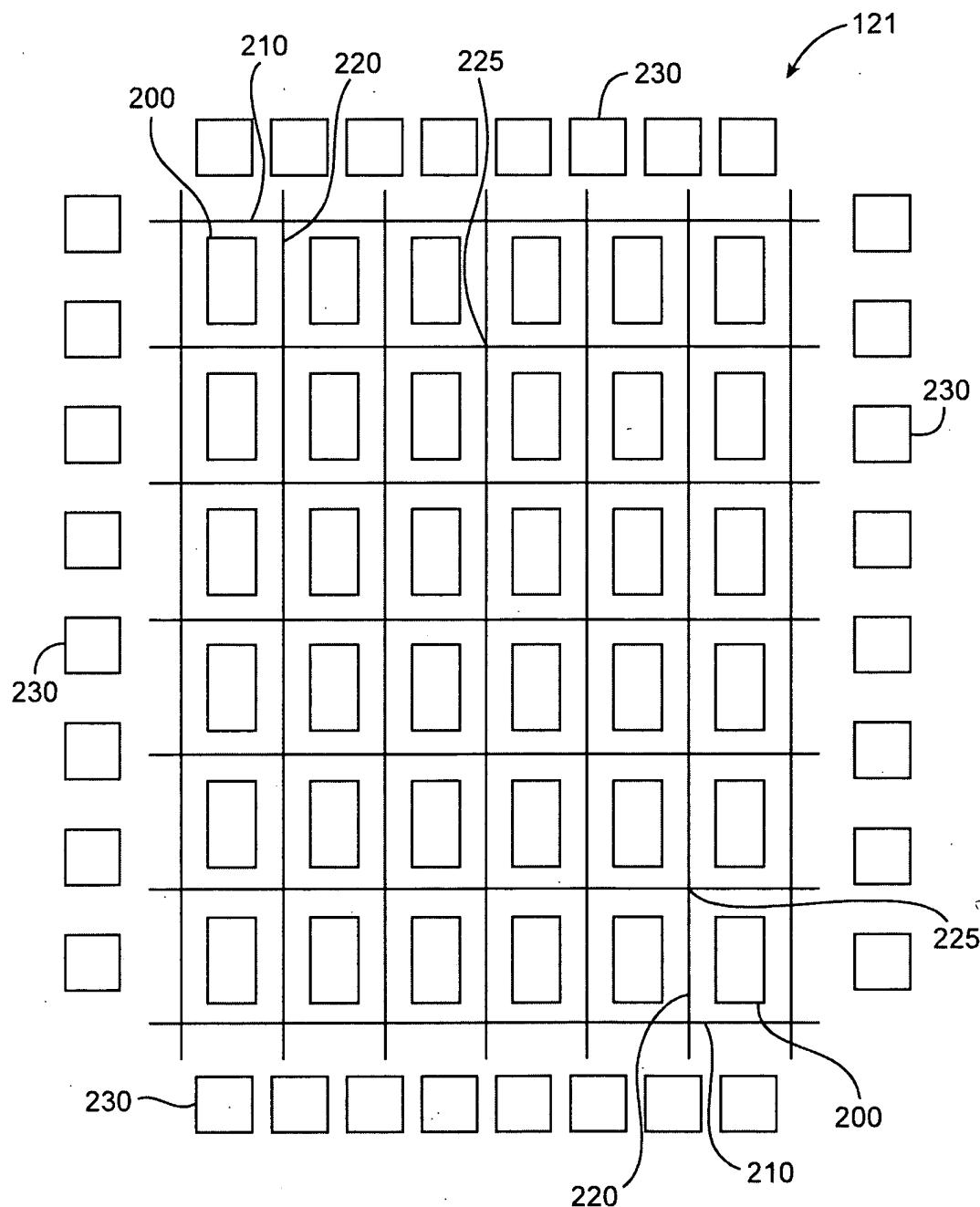


FIG. 2

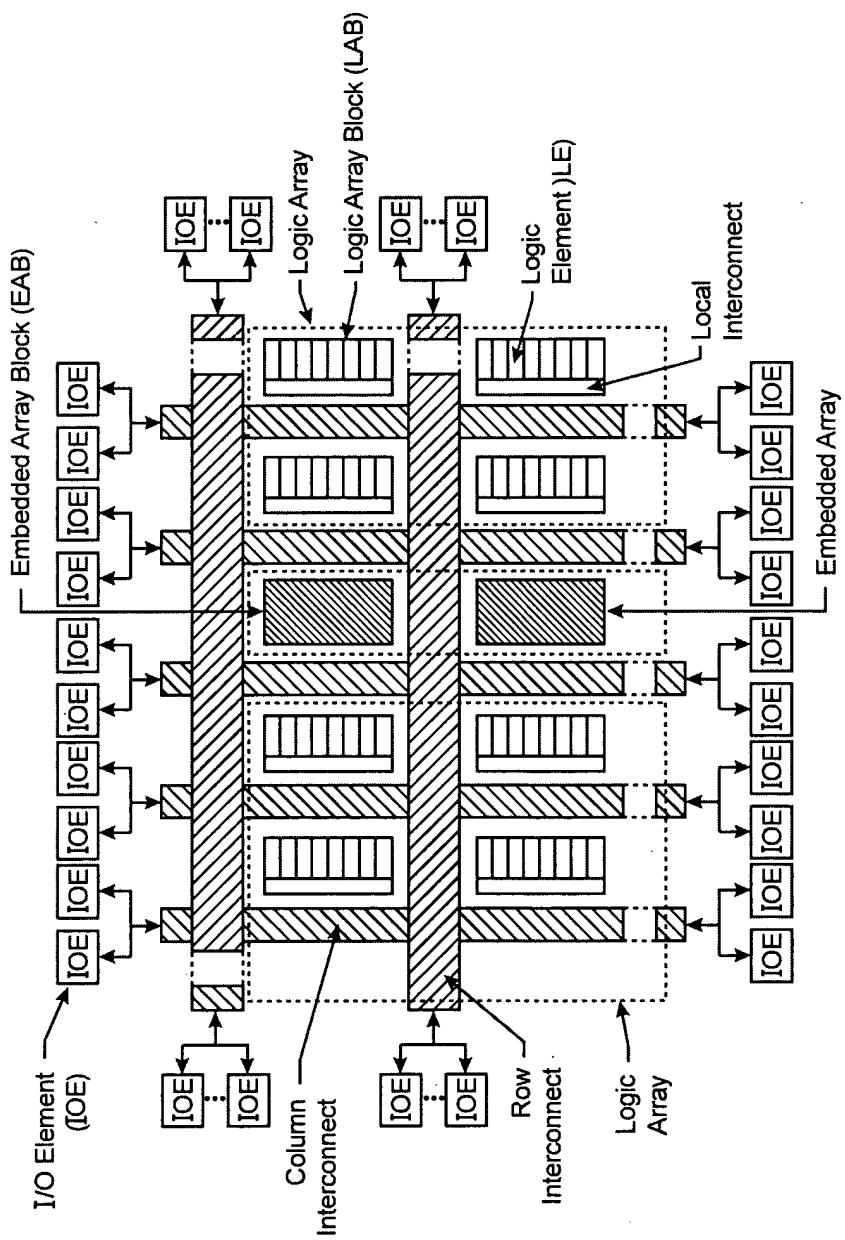


FIG. 4

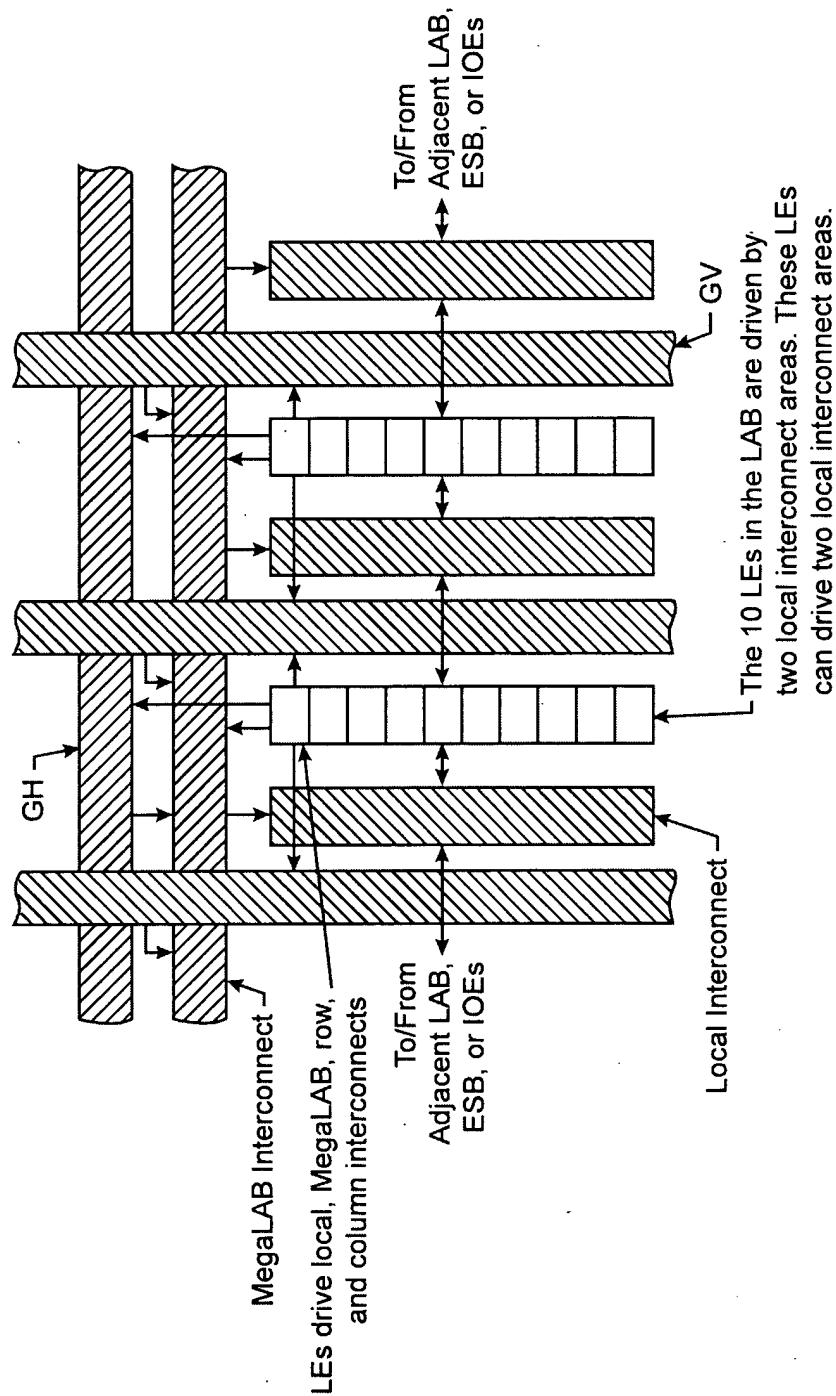


FIG. 5

5 / 10

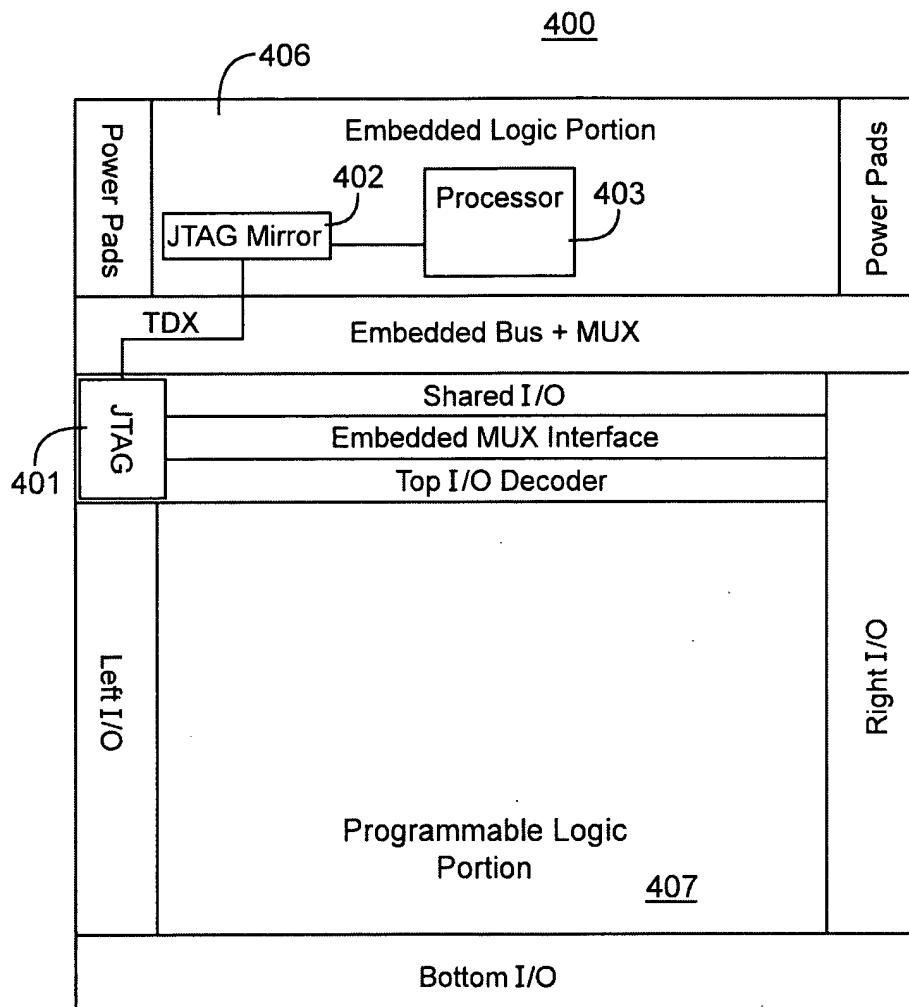


FIG. 6

6 / 10

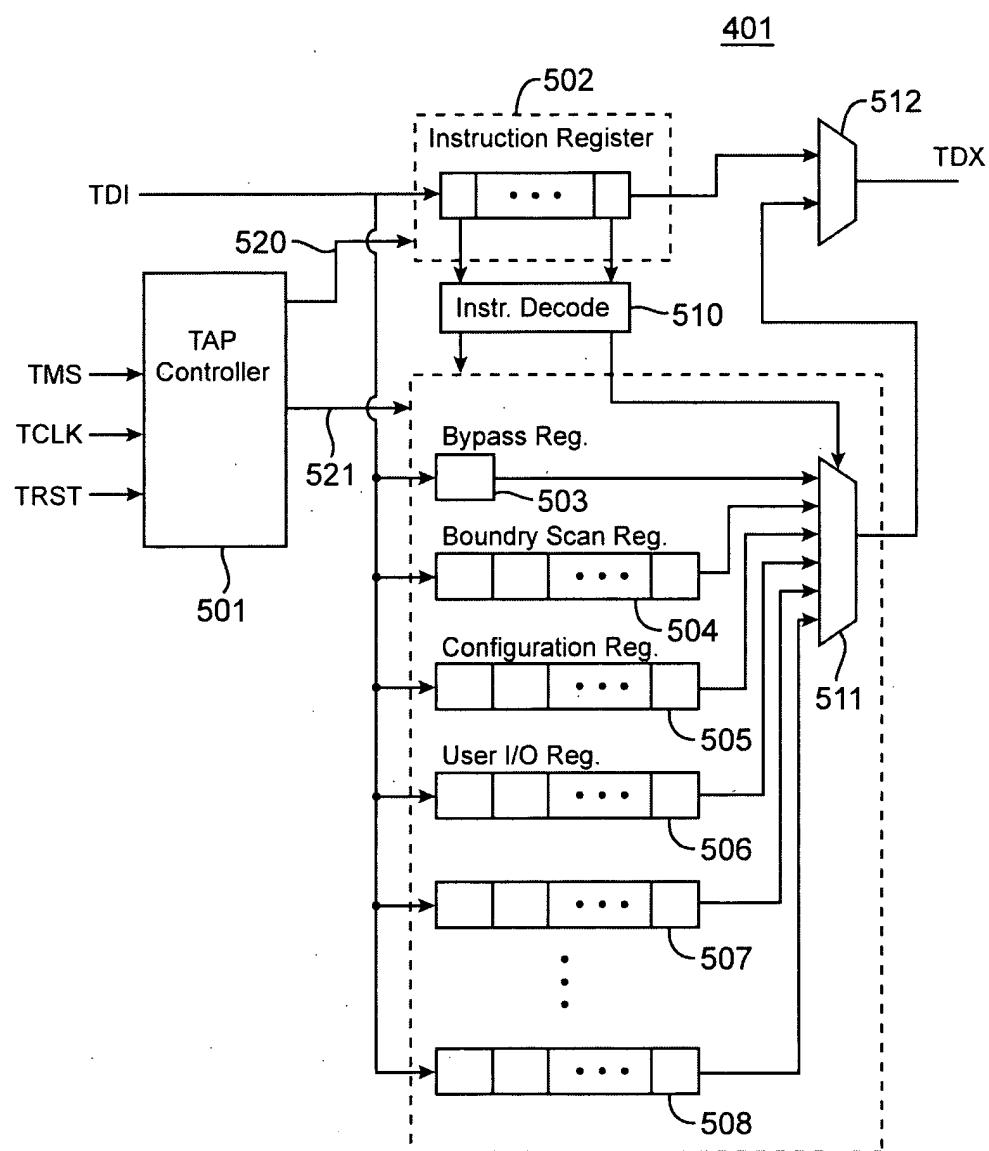


FIG. 7

7 / 10

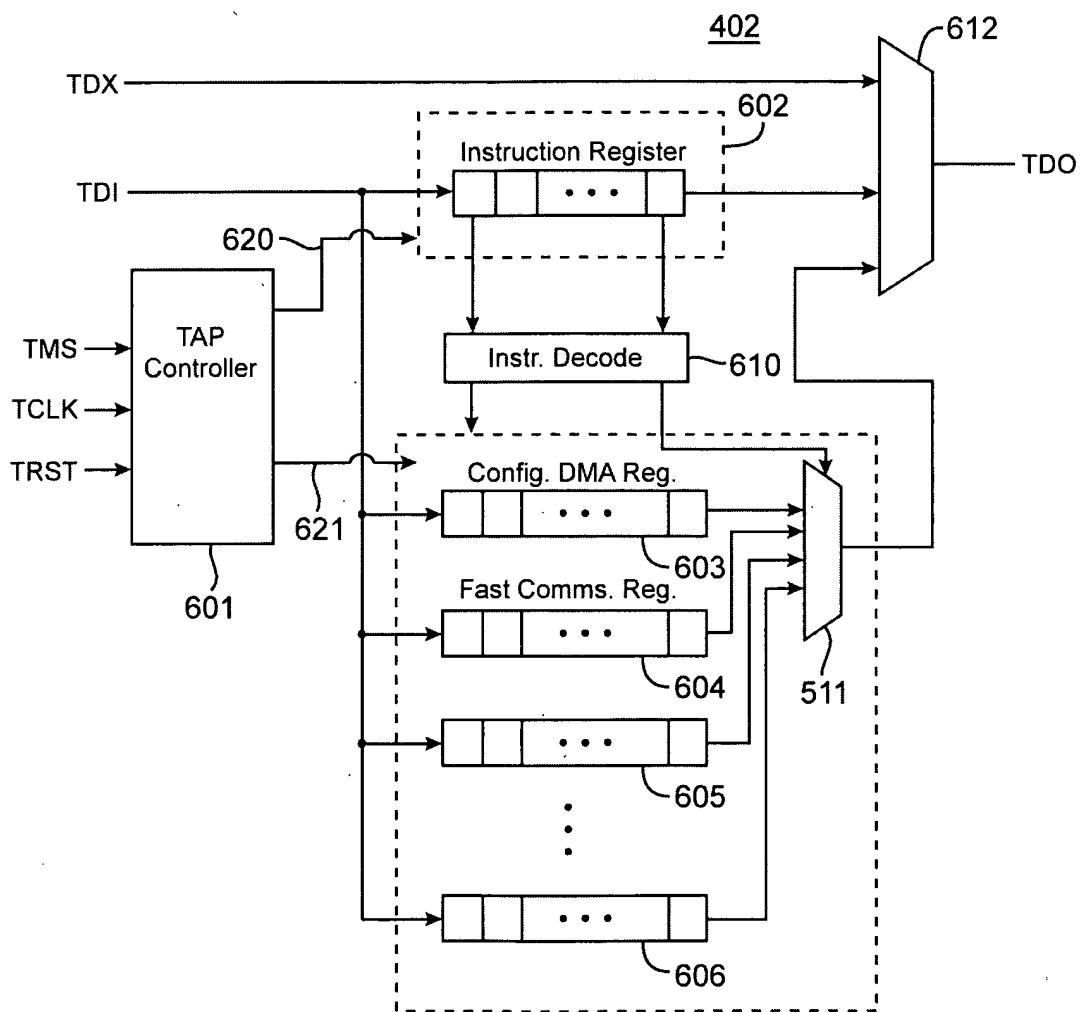


FIG. 8

8 / 10

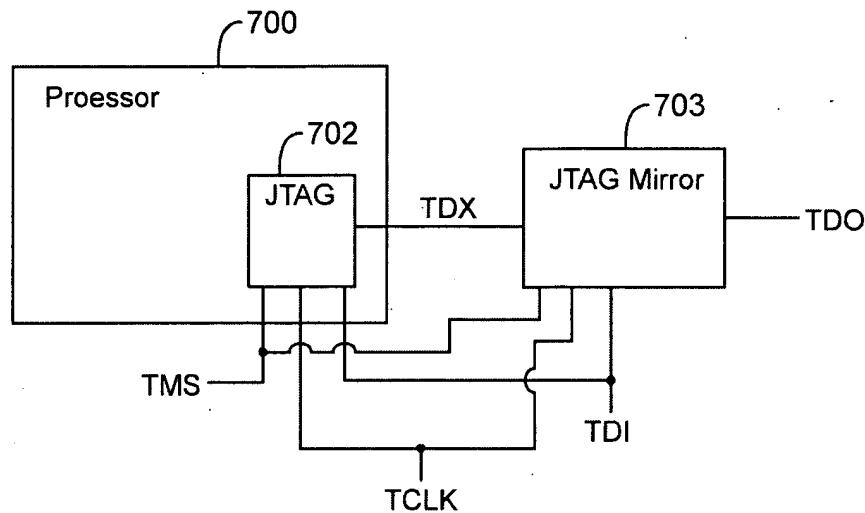


FIG. 9

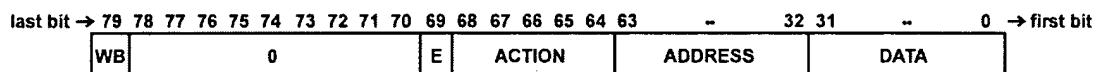


FIG. 10

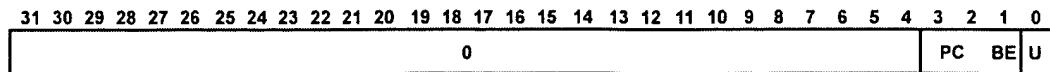


FIG. 11

9 / 10

last bit \rightarrow 69 .. 38 37 36 35 .. 3 2 1 0 Total Devices \rightarrow first bit

Captured Value:

READ DATA	RV WV	READ DATA	RV WV	1	0
-----------	-------	-----------	-------	---	---

last bit \rightarrow Total Devices 69 .. 38 37 36 35 .. 3 2 1 0 \rightarrow first bit

Update Value:

0	WRITE DATA	RD WR	WRITE DATA	RD WR	1
---	------------	-------	------------	-------	---

FIG. 12

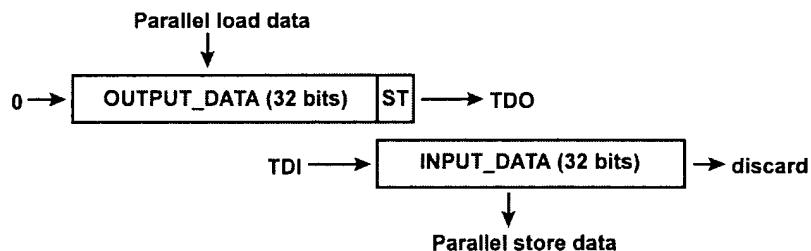


FIG. 13

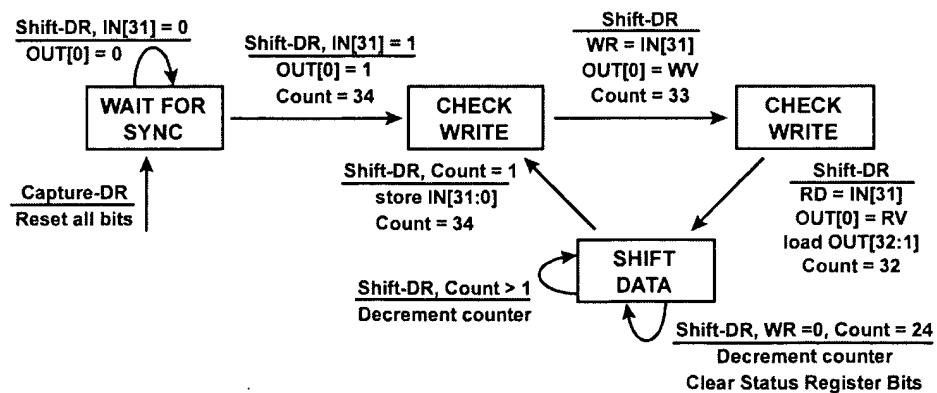


FIG. 14

10 / 10

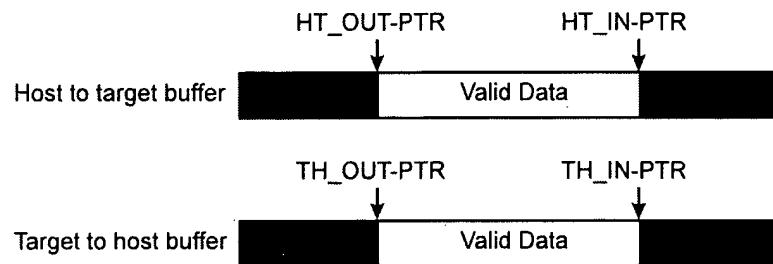


FIG. 15

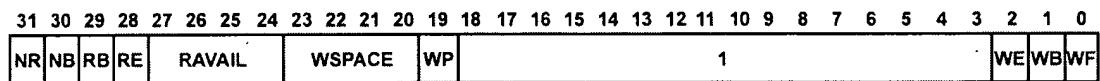


FIG. 16